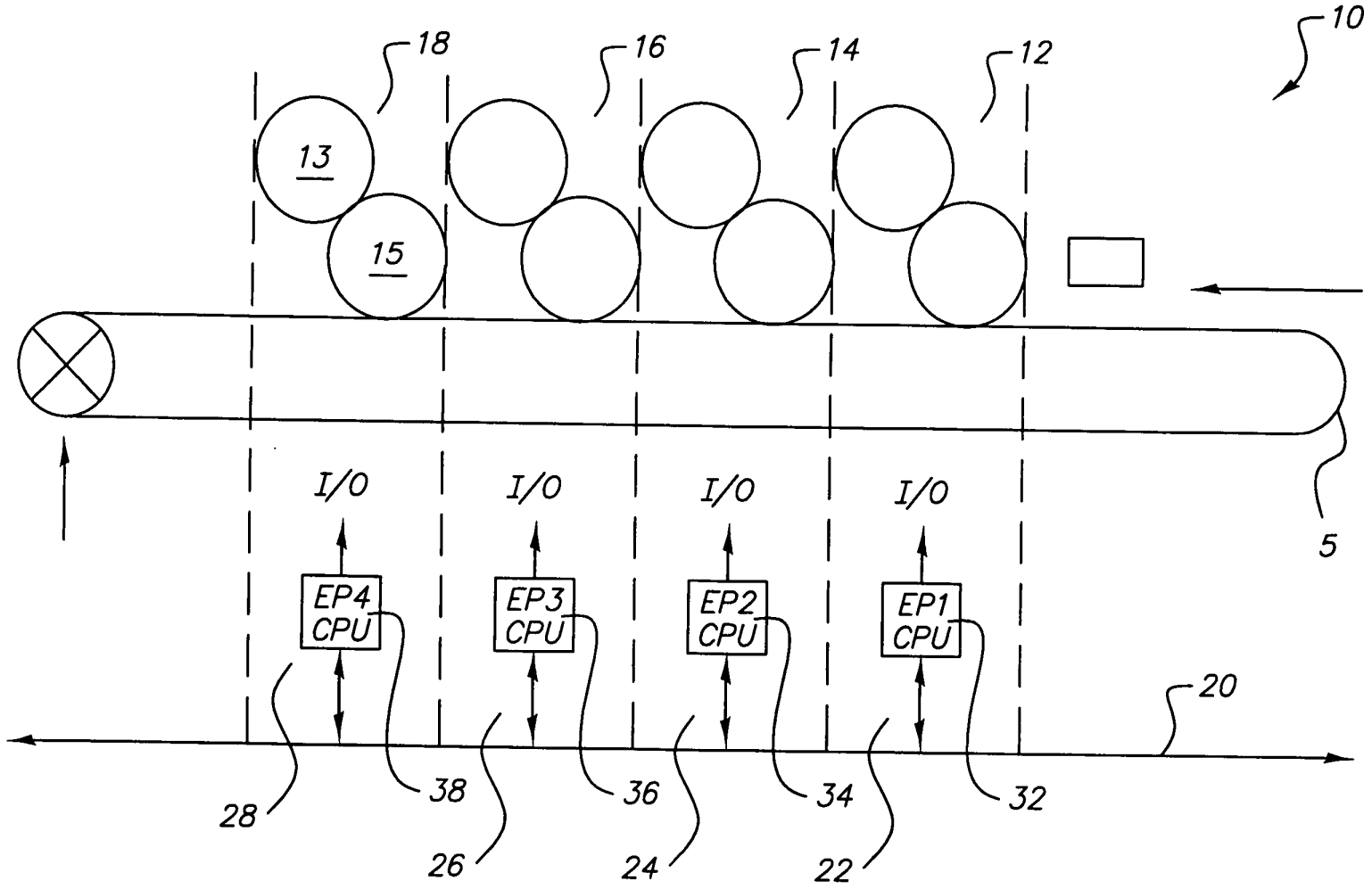
10
18
16
14
12
13
15
I/O
I/O
I/O
I/O
EP4 CPU
EP3 CPU
EP2 CPU
EP1 CPU
5
20
28
38
26
36
24
34
22
32

FIG. 1a

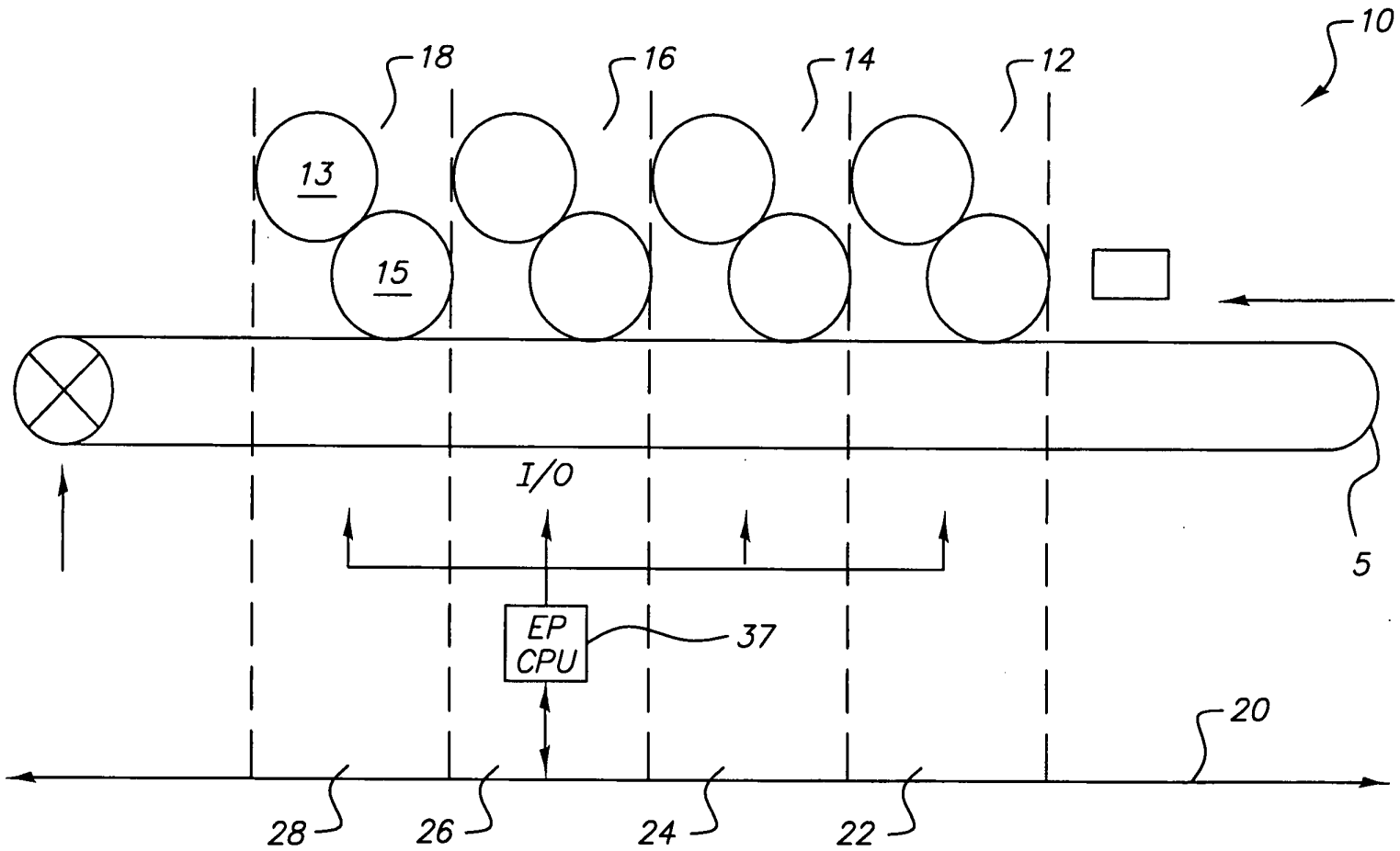

FIG. 1b

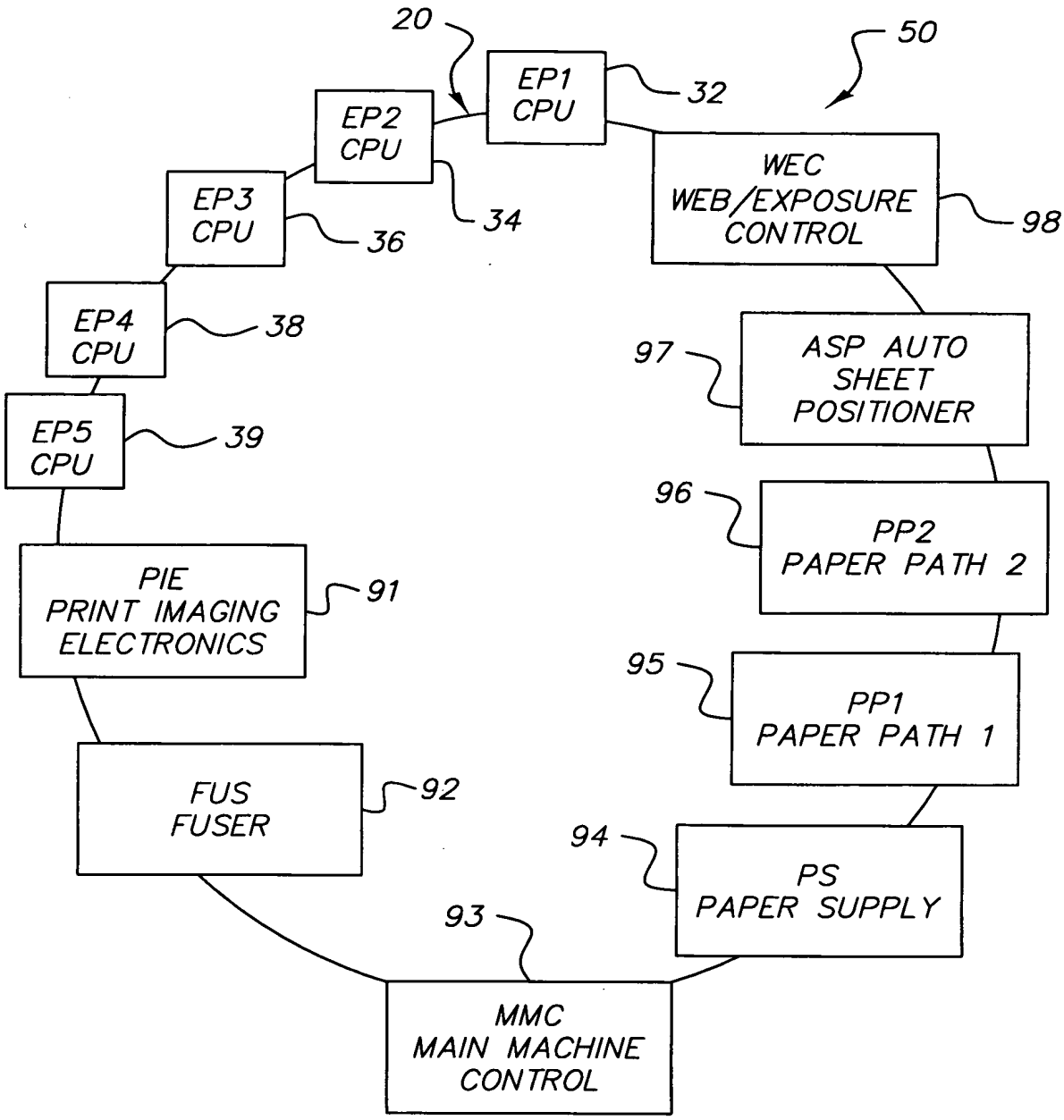
20
EP1
CPU
32
50
EP2
CPU
34
WEC
WEB/EXPOSURE
CONTROL
98
EP3
CPU
36
EP4
CPU
38
97
ASP AUTO
SHEET
POSITIONER
EP5
CPU
39
96
PP2
PAPER PATH 2
PIE
PRINT IMAGING
ELECTRONICS
91
95
PP1
PAPER PATH 1
FUS
FUSER
92
94
PS
PAPER SUPPLY
93
MMC
MAIN MACHINE
CONTROL

FIG. 2

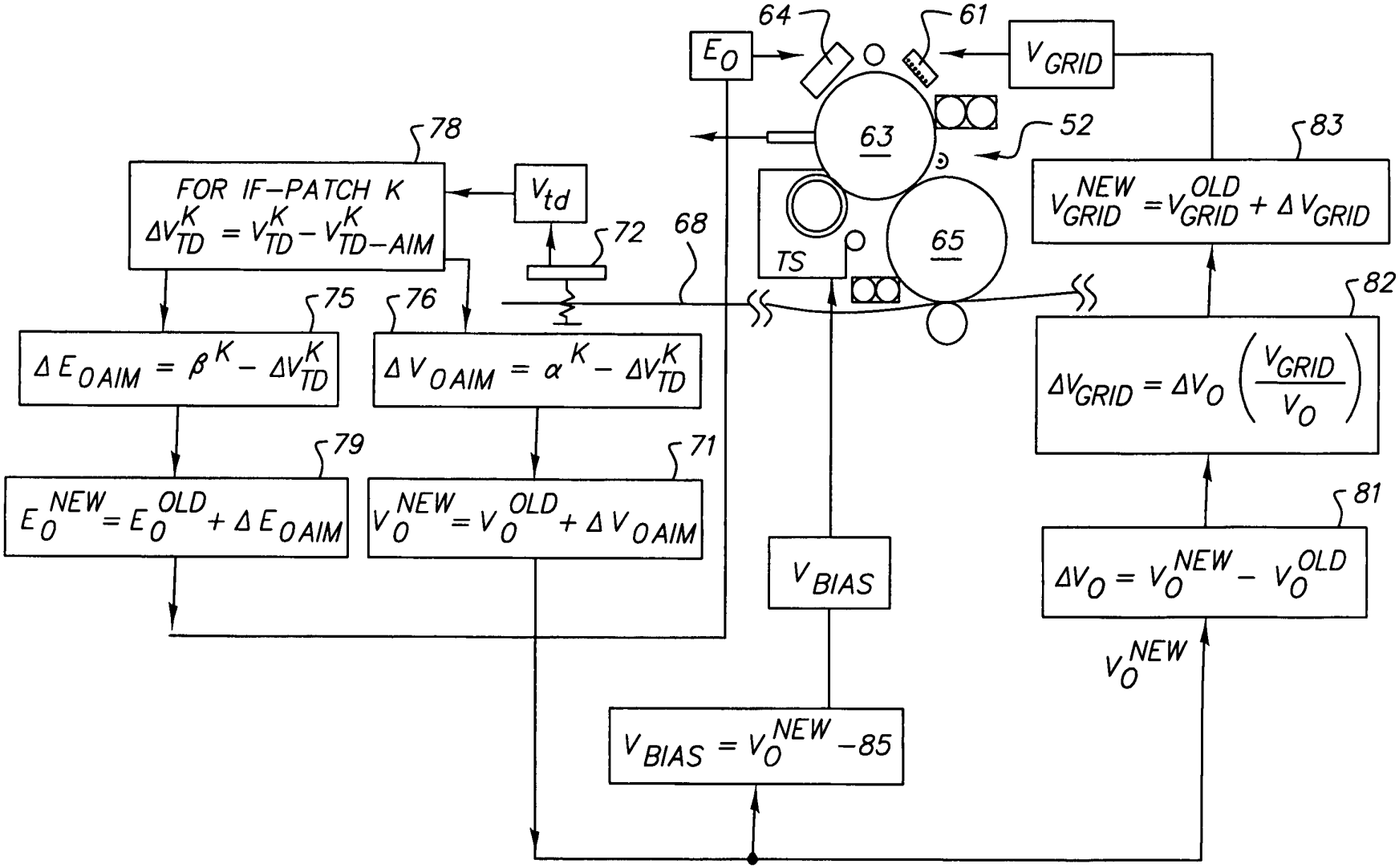
64
61
E_O
V_{GRID}
63
52
83
78
FOR IF-PATCH K
$\Delta V_{TD}^K = V_{TD}^K - V_{TD-AIM}^K$
V_{td}
72
68
TS
65
$V_{GRID}^{NEW} = V_{GRID}^{OLD} + \Delta V_{GRID}$
82
75
76
$\Delta E_{OAIM} = \beta^K - \Delta V_{TD}^K$
$\Delta V_{OAIM} = \alpha^K - \Delta V_{TD}^K$
$\Delta V_{GRID} = \Delta V_O \left(\frac{V_{GRID}}{V_O} \right)$
79
71
81
$E_O^{NEW} = E_O^{OLD} + \Delta E_{OAIM}$
$V_O^{NEW} = V_O^{OLD} + \Delta V_{OAIM}$
V_{BIAS}
$\Delta V_O = V_O^{NEW} - V_O^{OLD}$
V_O^{NEW}
$V_{BIAS} = V_O^{NEW} - 85$

FIG. 3